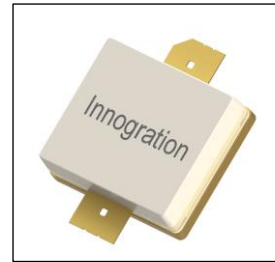


## 120W, HF to UHF, 28V High Power RF LDMOS FETs

### Description

The ITEV10240A2C is a 120-watt capable, high performance, highly rugged, unmatched LDMOS transistor, designed for any general applications at frequencies from HF to UHF, in new generation highly cost effective open cavity package.



- Typical CW performance with device soldered  
V<sub>ds</sub>= 28V, I<sub>dq</sub>=100mA(V<sub>gs</sub>=3.24V)

Freq	P1dB	P1dB	P1dB Eff	P1dB Gain	P3dB	P3dB	P3dB Eff
(MHz)	(dBm)	(W)	%	dB	(dBm)	(W)	%
190	50.18	104.2	63.0	20.5	51.17	131.0	68.8
200	50.23	105.4	67.2	21.35	51.35	136.6	74.0
215	49.47	88.4	69.5	22.81	50.9	123.1	78.9

### Features

- High Efficiency and Linear Gain Operations
- Integrated ESD Protection
- Excellent thermal stability, low HCI drift
- Large Positive and Negative Gate/Source Voltage Range for Improved Class C Operation
- Pb-free, RoHS-compliant

### Suitable Applications

- 30-88MHz (Ground communication)
- 54-88MHz (TV VHF I)
- 88-108MHz (FM)
- 136-174MHz (Commercial ground communication)
- Laser Exciter
- Synchrotron
- MRI
- Plasma generator
- Weather Radar

**Table 1. Maximum Ratings**

Rating	Symbol	Value	Unit
Drain--Source Voltage	V <sub>DSS</sub>	+110	Vdc
Gate--Source Voltage	V <sub>GS</sub>	-10 to +10	Vdc
Operating Voltage	V <sub>DO</sub>	+55	Vdc
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Case Operating Temperature	T <sub>c</sub>	+150	°C
Operating Junction Temperature	T <sub>j</sub>	+225	°C

**Table 2. Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case T <sub>c</sub> = 85°C, T <sub>j</sub> =200°C, DC test	R <sub>θJC</sub>	0.7	°C/W

**Table 3. ESD Protection Characteristics**

Test Methodology	Class

# ITEV10240A2C LDMOS TRANSISTOR

Document Number: ITEV10240A2C  
Preliminary Datasheet V1.0

Human Body Model (per JESD22--A114)	Class 2
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**Table 4. Electrical Characteristics** ( $T_A = 25\text{ }^\circ\text{C}$  unless otherwise noted)

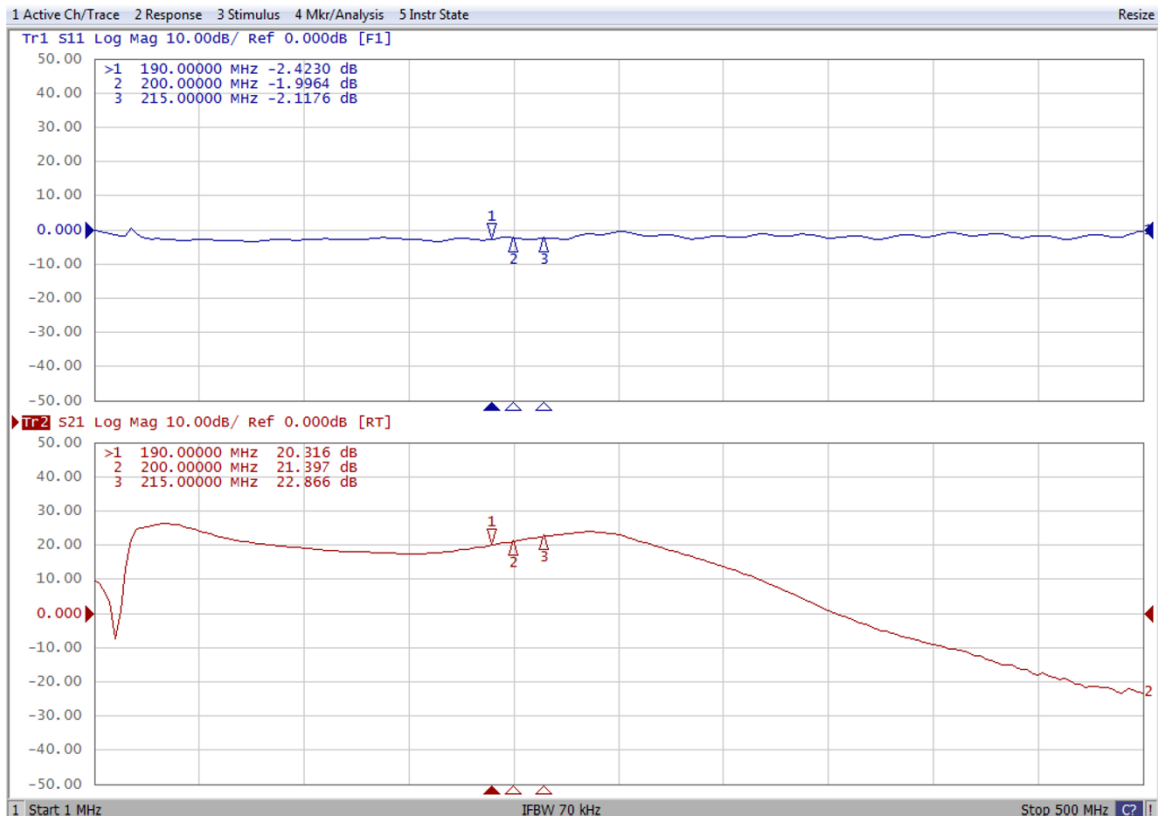
Characteristic	Symbol	Min	Typ	Max	Unit
Drain-Source Voltage $V_{GS}=0, I_{DS}=1.0\text{mA}$	$V_{(BR)DSS}$		110		V
Zero Gate Voltage Drain Leakage Current $(V_{DS} = 75\text{V}, V_{GS} = 0\text{V})$	$I_{DSS}$	—	—	1	$\mu\text{A}$
Zero Gate Voltage Drain Leakage Current $(V_{DS} = 28\text{V}, V_{GS} = 0\text{V})$	$I_{DSS}$	—	—	1	$\mu\text{A}$
Gate--Source Leakage Current $(V_{GS} = 10\text{V}, V_{DS} = 0\text{V})$	$I_{GSS}$	—	—	1	$\mu\text{A}$
Gate Threshold Voltage $(V_{DS} = 28\text{V}, I_D = 600\text{ }\mu\text{A})$	$V_{GS(th)}$	—	2.65	—	V
Gate Quiescent Voltage $(V_{DD} = 28\text{V}, I_D = 500\text{mA}, \text{Measured in Functional Test})$	$V_{GS(Q)}$	—	3.72	—	V

**Load Mismatch (In Innogration Test Fixture, 50 ohm system):**  $V_{DD} = 28\text{Vdc}$ ,  $I_{DQ} = 500\text{mA}$ ,  $f = 700\text{MHz}$ , pulse width:100us, duty cycle:10%

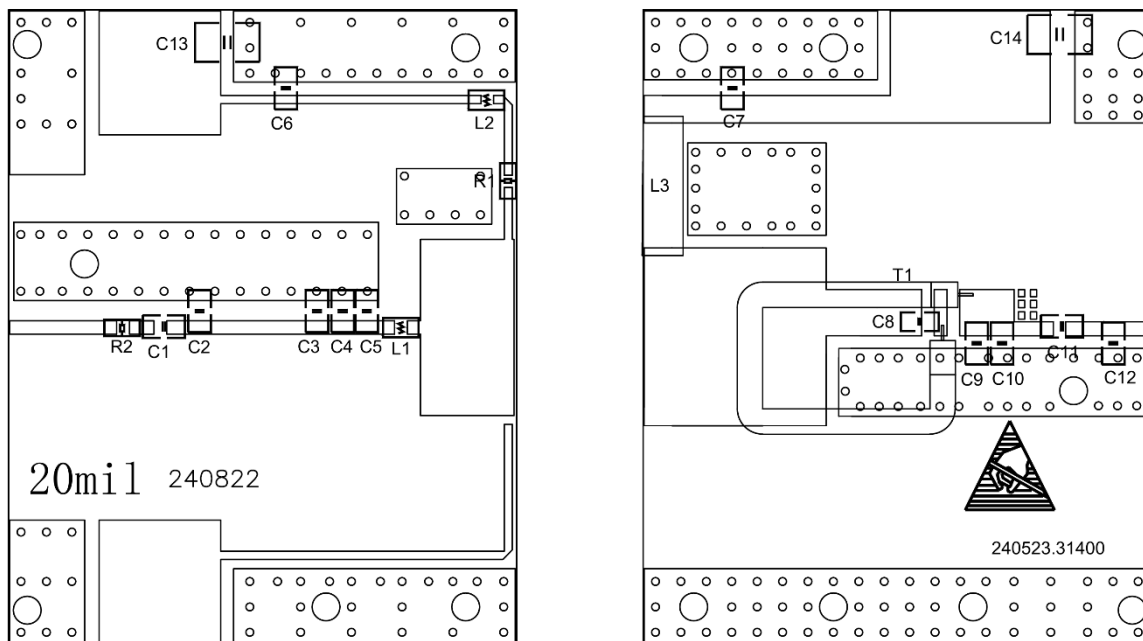
Load 10:1 All phase angles, at 120W Pulsed CW Output Power	No Device Degradation
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## TYPICAL CHARACTERISTICS

Figure 1: Network analyzer output S11/221



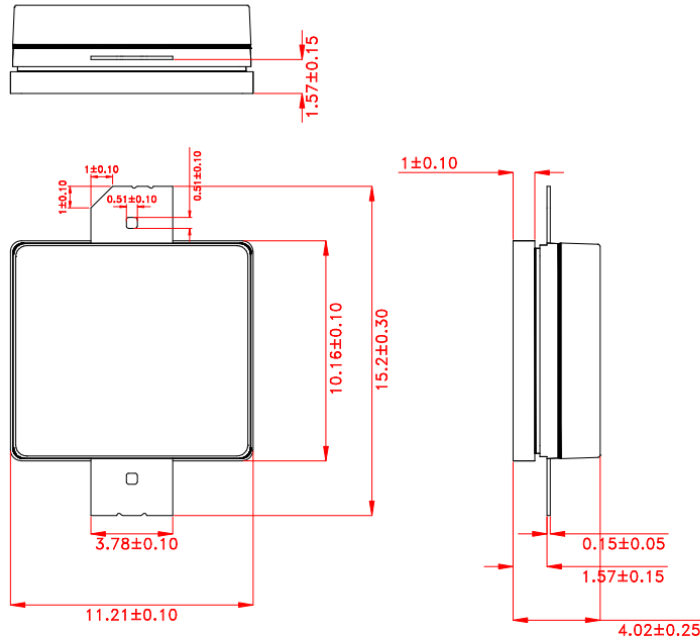
## Reference Circuit of Test Fixture Assembly Diagram



**Table 5. Test Circuit Component Designations and Values**

Reference	Footprint	Value	Quantity
C1, C6, C7, C11	0805	100pF/250V	4
C8	0805	150pF/250V	1
L1	0805	12nH	1
L2	0805	18nH	1
C2, C3, C9, C10	0805	8.2pF/250V	4
C4, C5	0805	12pF/250V	2
C12	0805	3.3pF/250V	1
T1		50ohm Coaxial line, length=90mm	1
C13, C14	1210	10uF/100V	2
L3		1.1mm wire, 4.1mm inner diameter, 12 turns	1
R1	0805	30R	1
R2	0805	3.3R	1
		ITEV10240A2C	1

## Package Dimensions



Unit:mm

Tolerance  $\pm 0.10$ mm, Except as Noted.

## Revision history

Table 5. Document revision history

Date	Revision	Datasheet Status
2024/11/1	Rev 1.0	Preliminary Datasheet Creation

Application data based on ZBB-24-48

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